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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/812,274	03/19/2001	Samuel M. Babb	10992052-1	2356

7590 02/11/2003

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EXAMINER

NGO, HUNG V

ART UNIT	PAPER NUMBER
2831	17

DATE MAILED: 02/11/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.  
09/812,274

Applicant(s)

Babb et al

Examiner

Hung V. Ngo

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

1)  Responsive to communication(s) filed on Dec 9, 2002

2a)  This action is FINAL.      2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

### Disposition of Claims

4)  Claim(s) 36, 38, 39, 41, 42, 44, 46-48, 50, and 53-71 is/are pending in the application.

4a) Of the above, claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 36, 38, 39, 41, 42, 44, 46-48, 50, and 53-71 is/are rejected.

7)  Claim(s) \_\_\_\_\_ is/are objected to.

8)  Claims \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are a)  accepted or b)  objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)  The proposed drawing correction filed on \_\_\_\_\_ is: a)  approved b)  disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12)  The oath or declaration is objected to by the Examiner.

### Priority under 35 U.S.C. §§ 119 and 120

13)  Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)  All b)  Some\* c)  None of:

1.  Certified copies of the priority documents have been received.

2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.

3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\*See the attached detailed Office action for a list of the certified copies not received.

14)  Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

a)  The translation of the foreign language provisional application has been received.

15)  Acknowledgement is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

### Attachment(s)

1)  Notice of References Cited (PTO-892)

4)  Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_

2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)

5)  Notice of Informal Patent Application (PTO-152)

3)  Information Disclosure Statement(s) (PTO-1449) Paper No(s). \_\_\_\_\_

6)  Other: \_\_\_\_\_

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## DETAILED ACTION

### *Claim Rejections - 35 USC § 112*

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 36, 38, 39, 41, 42, 44, 46-48 50, 53-71 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 36, lines 14-17, is not discussed in the specification. Any negative limitation or exclusionary proviso must have basis in the original disclosure See Ex parte Grasselli, 231 USPQ 393 (Bd. App. 1983)(MPEP 2173.05(i)). This limitation is not considered over the prior art in addressing the claims.

Claims 38, 39, 41, 42, 44, 46-48, 50, 53-71 are included because of their dependencies.

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claim 36, 38, 39, are rejected under 35 U.S.C. 102(b) as being anticipated by Heiss.

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Heiss discloses a printed wiring board (2); a plurality of components (1) mounted on said printed wiring board; and an electrically continuous conformal coating for providing an EMI-impervious shield conformingly and adheringly on the printed circuit board, including a conductive coating (6) that prevents the electromagnetic waves from passing therethrough, said conductive coating conformingly and adheringly coating the surface of one or more regions of the printed circuit board, wherein said conductive coating of each said region is electrically connected to each other, and a dielectric coating (7) interposed between said conductive coating and predetermined portions of each said printed circuit board region, wherein said dielectric coating completely insulates said predetermined portions of said printed circuit board region (re claim 36).

Re claim 38, wherein said regions of said conformal coating are physically contiguous.

Re claim 39, wherein said printed circuit board comprises a plurality of grounding pads (3) mounted in said printed wiring board, wherein said conductive coating is connected electrically to said grounding pads, wherein aid ground pads are electrically connected to a ground source of the printed wiring board.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior

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art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 36, 38, 39, 41, 42, 44, 46-48, 50, 53-71 are rejected under 35 U.S.C. 103(a) as being unpatentable over Suzuki in view of Heiss

Suzuki discloses a printed wiring board (200); a plurality of components (220) mounted on said printed wiring board; and an electrically continuous conformal coating for providing an EMI-impervious shield conformingly and adheringly on the printed circuit board, including a conductive coating (330) that prevents the electromagnetic waves from passing therethrough, said conductive coating conformingly and adheringly coating the surface of one or more regions of the printed circuit board, wherein said conductive coating of each said region is electrically connected to each other.

Re claim 38, wherein said regions of said conformal coating are physically contiguous.

Re claim 39, wherein said printed circuit board comprises a plurality of grounding pads (224, 225) mounted in said printed wiring board, wherein said conductive coating is connected electrically to said grounding pads, wherein aid ground pads are electrically connected to a ground source of the printed wiring board.

Re claim 41, wherein said printed circuit board further comprises a shielded connector mounted on said printed wiring board, said shielded connector connected to a shielded cable through which signals travel (col 1, lines 18-19)

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Re claim 44, wherein said printed circuit board has edge plating (226) connected electrically to said first and second regions of said conformal coating.

Re claim 46, wherein said electrical connection between said first and second regions is provided by a combination of: a ground land mounted on said top surface and said bottom surface of said printed wiring board proximate to the edge of said printed wiring board; and a plurality of electrically conductive spring clips (226) spaced around said printed wiring board so as to contact said ground lands on said top and bottom surfaces of said printed wiring board.

Re claim 47, wherein said electrical connection between said first and second regions is provided by a plurality of electrically conductive spring clips (226) spaced around said printed wiring board so as to contact said conductive coating of said first region and said conductive coating of said second region.

Re claim 48, wherein said printed wiring board comprises signal traces formed on the surface thereof, wherein one or more of the following features are selected alone or in combination such that said surface signal traces have a desired characteristic impedance: width of said surface signal traces; thickness of said surface signal traces; dielectric constant of said dielectric coating; and thickness of said dielectric coating (inherent).

Re claim 50, wherein one or more components are coated individually with a conformal EMI shield, wherein said component shield is electrically connected to said conformal coating on said printed circuit board

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Re claims 57-62, 64-66, the presence of process limitations in product claims, which product does not otherwise patentably distinguish over prior art, cannot impart patentability to that product. *In re Stephens* 145 USPQ 656 (CCPA 1965).

Claims 53-56, 67, 68, 71 have been addressed in previous paragraphs

The teaching of Suzuki as discussed above does not disclose a dielectric coating interposed between said conductive coating and predetermined portions of each said printed circuit board region, wherein said dielectric coating completely insulates said predetermined portions of said printed circuit board region (re claim 36, 37), a second region covering at least a portion of a bottom surface of said printed wiring board (re claim 42), specific thickness (re claims 63, 69, 70).

Re claims 63, 69, 70, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the structure of Suzuki by employing the specific thickness. A change in size is generally recognized as being within the level of ordinary skill in the art. *In re Rose*, 105 USPQ 237 (CCPA 1955).

Heiss discloses a dielectric coating (7) interposed between said conductive coating and predetermined portions of each said printed circuit board region, wherein said dielectric coating completely insulates said predetermined portions of said printed circuit board region and a second region covering at least a portion of a bottom surface of said printed wiring board.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to include the dielectric coating and the second region with the structure of

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Suzuki for the purpose of reducing the space requirement to a minimum as taught by Heiss (col 1, lines 55-58).

***Response to Arguments***

Applicant's arguments filed 12-09-02 have been fully considered but they are not persuasive.

Applicant argues (1) that Suzuki's conformal coating is not applied to printed circuit board, (2) the motivation to combine Suzuki and Heiss. The examiner disagrees. With respect to (1), portion 320 of the coating 330 is applied to the printed circuit board. With respect to (2), the examiner recognizes that obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either in the references themselves or in the knowledge generally available to one of ordinary skill in the art. See *In re Fine*, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988) and *In re Jones*, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992).

***Communication***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung V. Ngo whose telephone number is (703) 308-7614. The examiner can normally be reached on Tuesday to Friday from 8:30 am to 06:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean Reichard, can be reached on (703) 308-3682.

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The fax phone number for this Group is (703) 305-3431 or (703) 305-3432.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.

Hung V. Ngo

*Hung V. Ngo*

February 9, 2003

HUNG V. NGO  
PATENT EXAMINER